

JCM37

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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10001977	FILING DATE 12/05/2001	CLASS 438	SUBCLASS 1	GAU 2812	EXAMINER UP Perkins
<b>**APPLICANTS:</b> Tanimoto Hiroyoshi; Enda Toshiyuki;					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>**FOR EN APPLICATIONS VERIFIED:</b> JAPAN 2001-313939 10/11/2001					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO			
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		216648US2			
Verified and Acknowledged Examiners's initials					
TITLE : Semiconductor device, method of manufacturing semiconductor device, and system for evaluating electrical characteristics of semiconductor device					

U.S. DEPT. OF COMM/PAT & TM-PTO-415US Rev. 12-94

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>Assistant Examiner</b>	<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>Primary Examiner</b>	<b>DRAWING</b>	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	<b>Application Examiner</b>	
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